

**GOVERNMENT COLLEGE OF ENGINEERING, JALGAON**

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Name of Examination : **Winter 2020** - (Preview)Course Code & Course Name : **CO401 - Compiler Construction**Generated At : **19-04-2022 11:07:41**Maximum Marks : **60**Duration : **3 Hrs**[Edit](#) [Print](#) [View Answer Key](#) [Close](#) **Answer Key Submission Type:** Marking scheme with model answers and solutions of numerical

Instructions:

1. All questions are compulsory.
2. Illustrate your answer with suitable figures/sketches wherever necessary.
3. Assume suitable additional data; if required.
4. Figures to the right indicate full marks.

1) Solve any two sub-question

- a) Draw and explain the phases of Compiler. [6]
- b) Explain LEX in detail. [6]
- c) Discuss the different types of language processors. [6]

2) Solve any two sub-question.

- a) Explain recursive descent parsing in detail. [6]
- b) Explain operator precedence parsing. [6]
- c) Explain in detail LALR PARSER. [6]

3) Solve any two sub-question.

- a) Explain Semantic Analysis and Syntax directed Translation. [6]
- b) What is three address code? Explain. [6]
- c) Explain in detail the quadruples. [6]

4) Solve all sub-question

- a) What are different storage allocation strategies? [6]
- b) What are the contents of activation record? [6]

5) Solve all sub-question

- a) Explain basic blocks and flow graph. [6]
- b) Explain the peephole optimization. [6]

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